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Date: 22 April 2004

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Applicant

Dagobert M. De Leeuw, et al.

Appl. No.

09/817,107

26 March 2001

Grp./Art Unit

: 2822

Examiner

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Title

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Integrated Circuit Provided With A; Substrate And With A Memory,

Transponder, And Method Of **Programming A Memory**

Docket No. :

NL000149

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